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Attorney Docket No. PATENTS 174/183

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
PATENT APPLICATION

Applicants : Edward Aung et al.
Application No.: 09/805,843 Confirmation No.: 3795
Filed : March 13, 2001
For : CLOCK DATA RECOVERY CIRCUITRY ASSOCIATED
WITH PROGRAMMABLE LOGIC DEVICE CIRCUITRY
Group Art Unit : 2182

Hon. Commissioner for Patents
P.O. Box 2327
Arlington, VA 22202

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DEC 10 2002
Technology Center 2100

SECOND PRELIMINARY AMENDMENT

Sir:

Please amend the above-identified patent
application as follows:

In the Claims

Enter additional claims 122-127 as follows:

122. (New) Apparatus for receiving and processing a
plurality of CDR signals, each of which includes data
information and clock information, comprising:

first circuitry producing a plurality of
candidate recovered clock signals from a reference clock
signal, the candidate recovered clock signals having phases
that are shifted relative to one another; and

a plurality of second circuitries, each
receiving a respective one of the CDR signals and using the
candidate reference clock signals to recover the clock
information from the CDR signal received by that second
circuitry.

12/09/2002 CNGUYEN 00000101 09805843

01 FC:1201
02 FC:1202

84.00 OP
108.00 OP